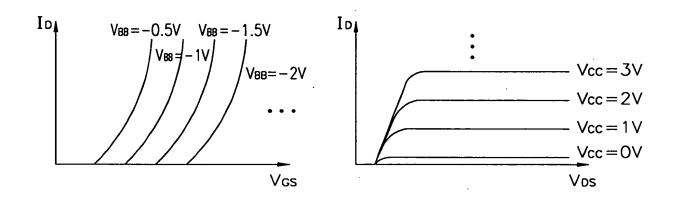
FIG. 1



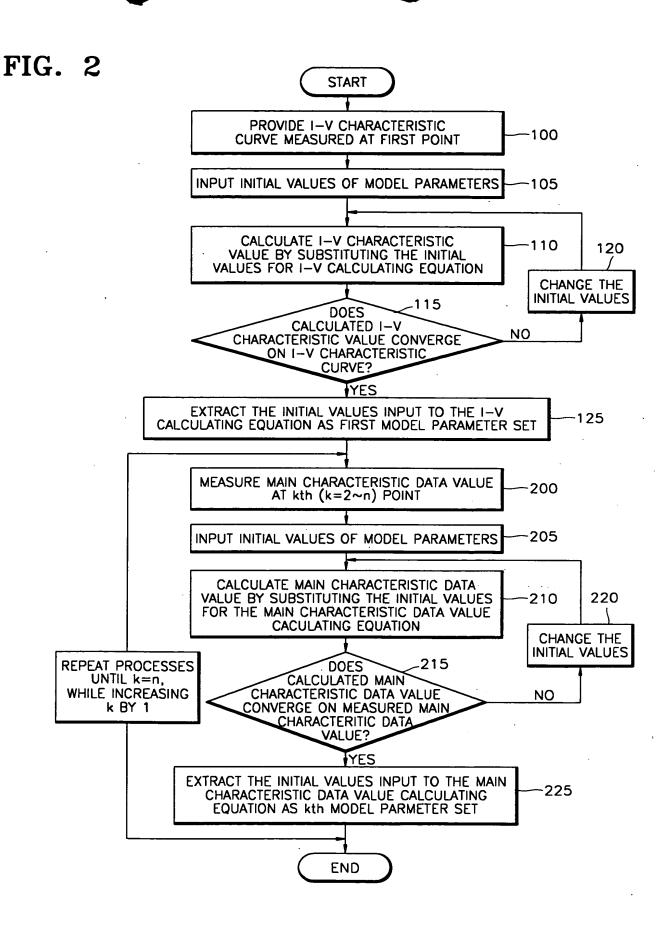


FIG. 3

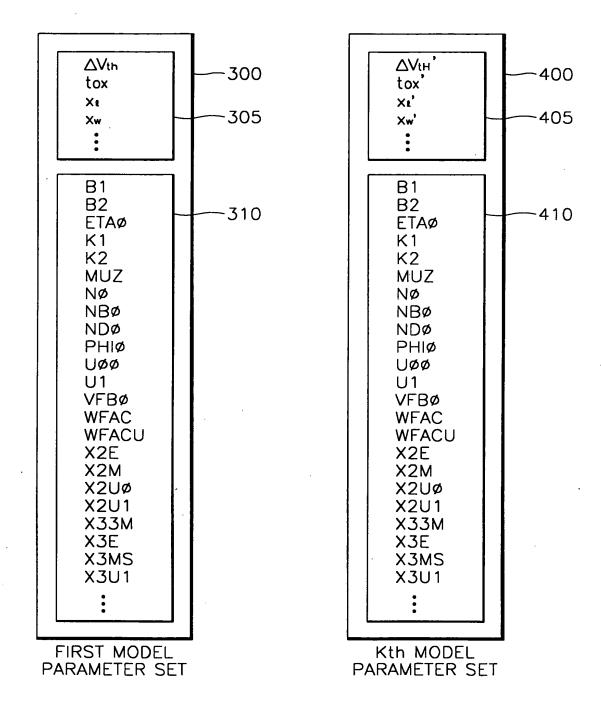


FIG. 4

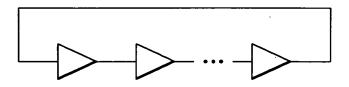
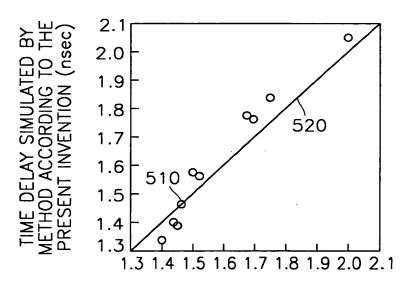


FIG. 5



TIME DELAY SIMULATED BY CONVENTIONAL METHOD (nsec)